

Agenda

15th Power Analysis & Design Symposium - April 29th, 2026



| | Central European Summer Time (CEST/UTC +2) (Vienna, Berlin) | Eastern Daylight Time (EST/UTC-4) (New York) | Hong Kong Time (HKT/UTC+8) |
|---|--|---|----------------------------|
| <i>Welcome and introduction</i> | 09:00 / 09:00 am | 03:00 am | 15:00 / 03:00 pm |
| Trade Secrets of the Flyback Converter by Christophe Basso - Future Electronics | 09:10 / 09:10 am | 03:10 am | 15:10 / 03:10 pm |
| <i>10 min break</i> | | | |
| Unleash the Power of the Smith Chart: An Intuitive Guide for RF and Non-RF Engineers by Arturo Mediano - University of Zaragoza | 10:20 / 10:20 am | 04:20 am | 16:20 / 04:20 pm |
| <i>10 min break</i> | | | |
| "Capacitances" of Nonlinear Capacitors by Sam Ben-Yaakov - IRP Systems | 11:20 / 11:20 am | 05:20 am | 17:20 / 05:20 pm |
| <i>1 h break</i> | | | |
| Hands-On Challenges and Solutions in Bidirectional GaN Switch Design by Milan Marjanovic - Electronicdesign-Marjanovic | 13:10 / 01:10 pm | 07:10 am | 19:10 / 07:10 pm |
| <i>10 min break</i> | | | |
| DC Biased Impedance Measurements by Florian Hämmerle - OMICRON Lab | 14:10 / 02:10 pm | 08:10 am | 20:10 / 08:10 pm |
| <i>10 min break</i> | | | |
| Practical EMC Debugging of Power Supplies: Measurements, Ringing, and Noise Sources by Ali Shirsavar - Biricha Digital | 15:10 / 03:10 pm | 09:10 am | 21:10 / 09:10 pm |
| <i>10 min break</i> | | | |
| Unveiling the Mathematics Behind NISM: A New Path to Power Integrity Stability by Steve Sandler - Picotest | 16:10 / 04:10 pm | 10:10 am | 22:10 / 10:10 pm |